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Receiver Module is controlled by the

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host via a standard TTL-level, asynchronous serial communications interface. A single pin (SIO) transfers commands sent TO the Module and data received FROM the Module. All communication is at 4800 bps, 8 data bits, no parity, 1 stop bit, non-inverted. Parallax GPS Receiver Module (#28146) View and Download Navman LA000508 ...

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ROM chip on GPS module. This is  
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This GPS receiver module kit includes an extremely sensitive patch antenna, with factory pre-soldered Mediatek

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chipset RF module. It has three onboard LEDs for visual status indication, 3.3V regulator and power supply filtering, and 5V logic level conversion. It's easy to build and directly compatible with all QRP Labs kits.

GPS receiver kit QL1 - QRP Labs  
The R4 Series GPS receiver module is a self-contained high-performance GPS receiver. Based on the SiRFstar IV chipset, it provides exceptional sensitivity, even in dense foliage and urban canyons. The module 's very low power consumption helps maximize runtimes in battery powered applications.

R4 Series GPS Receiver Module Data  
Guide

The 6-way set of connections

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provides access to the full set of signals, for general purpose use of this GPS receiver module. Note that TXD is the serial data output of the module, at 5V Logic levels. The 1 pulse per second (1pps) timing signal is also 5V.

QLG1 GPS Receiver kit - QRP Labs

This manual applies to all ANTARIS®4 GPS receivers based on Firmware 5.00 and above. u-blox AG Zürcherstrasse 68 8800 Thalwil Switzerland [www.u-blox.com](http://www.u-blox.com) Phone +41 44 722 7444 Fax +41 44 722 7447 [info@u-blox.com](mailto:info@u-blox.com)

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