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IA-64 is a high-performance microprocessor architecture co-designed by HP and Intel. The titanium chip is the first implementation of this new architecture.

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Intel already has outlined a roadmap of several chips to follow, such as the more powerful McKinley and Deerfield implementations.

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The book starts with an overview of Linux and the IA-64

architecture and then discusses each major subsystem of the kernel in more detail. Among others, there are separate chapters on how

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multi-tasking
maps onto the
underlying
hardware, on the
virtual memory
subsystem,
device support
(programmed I/O,
DMA &
interrupts),
symmetric multi-
processing
(SMP), and on
the bootstrap

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The Linux kernel
on IA64,
includes a
subsystem called
perfmon to
provide access
to the IA-64
PMU. The perfmon

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interface
consists of a
single system
call, perfmonctl
() which
supports a set
of...

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virtual

environment that

makes it appear

as if each

process had the

entire address

space of the CPU

available to

itself. This

virtual address

space extends

from address 0

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Linux, but also
to share some of
the excitement
and the creative
processes that
are involved in
solving the many
technical
challenges that
arise when
designing an
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for a platform
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I/O, symmetric

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authors of
device drivers,
and application
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descriptions of
the Linux
hardware

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On Linux/ia64,
switching the
address space
involves three
simple steps:
Set current page-
table pointer to

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the global
directory of the
new address
space. Ensure
that the new
address space
has a valid ASN
(region ID) by
calling `get_mmu-
context()`.

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in the IA-64...

Kernel Identify
Mappings ¶.

Linux/ia64

identity

mappings are

done with large

pages, currently

either 16MB or

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to as

"granules."

Cacheable

mappings are

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Linux Kernel [2],
so the processor
can read any
location in the
page at any
time,
independent of
the programmer's
intentions.

*Memory Attribute
Aliasing on
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Linux Kernel ...*

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