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Algorithms On Speech recognition is a
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dialectology that develops technologies
and methods that assists the recognition
and conversion of speech to text by the
computers. Implementation of Speech
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Implementing Speech Recognition

Algorithms On Speech recognition is a multifaceted subfield of computational dialectology that develops technologies and methods that assists the recognition and conversion of speech to text by the computers. Implementation of Speech Recognition and Analysis

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Implementation of Speech Recognition
Systems Spring 2011 Bhiksha Raj, Rita

Singh Class 1: Introduction 19 Jan 2011.

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... • Improvements in recognition algorithms • Availability of more and more training data ... • Speech recognition is a type of pattern recognition problem

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speech-recognition algorithms, we need a
basis for comparison. In this study, we
consider two: the Hidden Markov Model
Toolkit (HTK) [5], and the Probabilistic

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Modeling Toolkit for Matlab/Octave (pmtk3) [6]. HTK is a research implementation, written in C for high performance. As such, it represents a real-world example of

Implementing a Speech Recognition
Algorithm with VSIPL++

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MFCC code (Matlab implementation) —
written by Dan Ellis HTK (C-based
research implementation) — from
Cambridge University Engineering
Department (CUED) D.M., Implementing
a Speech Recognition Algorithm with
VSIPL++, September 2011

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Implementing a Speech Recognition
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The speech recognition is achieved by the concept of data science. It is one of the most sought after domain these days.

Scientific methods, systems, algorithms and processes are implemented to mine knowledge and insights from structured

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Implementation of Speech Recognition
and Analysis using ...

The speech recognition is achieved by the
concept of data science. It is one of the
most sought after domain these days.

Scientific methods, systems, algorithms

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and processes are implemented to...

Implementation of Speech Recognition
and Analysis using ...

The experimental results show that the real-time speech recognition algorithm proposed in this paper can improve the real-time recognition speed of the system.

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The corresponding speed changes from about 12 s to about 200 ms, and the corresponding real-time accuracy rate increases to about 95%.

Real time speech recognition algorithm on embedded system ...

Many algorithms have been proposed to

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implement speech recognition. These methods are autocorrelation, cross-correlation, spectrum normalization, Wiener Filter, and hidden Markov model. This research investigates several approaches for implementing the speech recognition system of isolated

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Analysis of Voice Recognition Algorithms
using MATLAB

short time speech spectrum covering a time period. The dictionary database consists of a sequence of words, = 1, 2, ?, ?. The speech recognition system is used to select the best word sequence for given the observed acoustic signal . Bayes? rule

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is used to implement this idea, = arg max
 $W = \operatorname{argmax}. W$

On Speech Recognition Algorithms

Speech recognition is an interdisciplinary subfield of computer science and computational linguistics that develops methodologies and technologies that

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enable the recognition and translation of spoken language into text by computers. It is also known as automatic speech recognition (ASR), computer speech recognition or speech to text (STT).

Speech recognition - Wikipedia

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Speech Recognition Algorithm in VC++ |
With the continuous development of
speech recognition algorithms, the
recognition rate is increasing, and can be
applied ...

Implementation of Speech Recognition
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viterbi algorithm on soft core processor, NIOSII[17]. Liu, H et al. implemented an English speech recognition system on a chip, called speech system-on-chip (SoC)[18]. In 2011, an embedded speech recognition system was implemented to base on the Cyclone II of FPGA and the soft-core processor of NIOS-II by Zhang,

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Hardware Implementing of a New
Decoding Algorithm HGSCA ...
algorithms to show the advantage of the
proposed algorithm. Moreover, the
implementation of the proposed speech rec-
ognition system on an FPGA-based

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embedded system with SOC architecture is explored. Some experiments of speech recognition on the hardware are then revealed. Finally, some conclusions are given in Sect. 6.

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