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Inside every SP601 is a 1.5kW, 2.0kW or 3.0kW flouropolymer coated element, an over temperature cut-out device, a temperature sensor, a water sensor, and a controller circuit board which includes an isolating transformer.

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o Hardware User Guide — Detailed description of the SP601 Board features, jumper settings, options and pin location tables. o Reference Design User Guide — Step by Step guide on understanding, implementing, modifying and customizing the SP601 Reference Designs. o Board Design Files — Schematics/Gerbers/BOM • Cables & Power Supply.

Spartan-6 FPGA SP601 Evaluation Kit FAQ

SP601 Setup. Connect a USB Mini-B Cable to the USB JTAG connector on the SP601 board. — Connect this cable to your PC. Compile MultiBoot Design. Unzip the rdf0006.zip file. Note: Presentation applies to the SP601. Compile MultiBoot Design. At a command prompt, type: cd sp601_multiboot_run_all_build_script.bat.

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CiteSeerX — SP601 Reference Design User Guide

SP601 BIST Design Description. Description. — The Built-In System Test (BIST) application uses an EDK MicroBlaze system to verify board functionality. A UART based terminal program interface offers users a menu of tests to run. Reference Design IP.

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